

LDO ICs

Low Voltage, High PSRR Low-Dropout 300mA Linear Regulator



AS9107

● General Description

The AS9107 is a CMOS low dropout, low noise, high PSRR, low quiescent current positive linear regulator with input voltage down to about 1.20V. The AS9107 can deliver a guaranteed 300mA load current with a low dropout voltage at 300mV that optimized for battery-powered systems or portable wireless devices such as mobile phones.

The device is suitable for portable application such as cellular handsets or PDA. The AS9107 is designed and optimized to work with low cost ceramic capacitors 1.0uF, which consumes less than 5.0 μ A during operation mode. Besides, its current limit protection and on-chip thermal shutdown function provide protection against any combination of over-load or ambient temperature which could cause junction temperature exceeding maximum rating. The AS9107 includes a reference bypass pin in order to reduce output noise and a logic control shut-down input.

The space-saving tiny SOT23-3L, SOT89-3L, SOT23-5L and DFN1010-4L packages are attractive for hand-held applications. The device is specified over an ambient temperature range of -40°C to 150°C.

● Features

- Output Current Up to 300mA
- Low Voltage V_{IN} Range: 2.0V to 5.5V
- Very Low Input Voltage at 1.20V (Min.)
- Very Low Quiescent Current at 5.0 μ A
- Low Dropout voltage of 300mV at 300mA
- Output Voltage Accuracy at $\pm 2.0\%$
- PSRR 70dB at 1.0KHz
- Needs Only 1.0uF Capacitor for Stability
- Current Limit Protection
- Internal Short-circuit current limit
- Thermal Shutdown
- Without EMI and Switch Noise
- Low ESR Ceramic Capacitor for Output Stability
- RoHS and Green Compliant
- SOT23-5L, SOT23-3L, SOT89-3L and DFN1010-4L Packages
- -40°C to +150 °C Temperature Range

● Applications

- PDAs and Digital Camera
- White LED Biasing
- Mobil Handsets
- Tablet PCs and Laptops/Netbooks
- Camcorder Video Light (Movie Light)

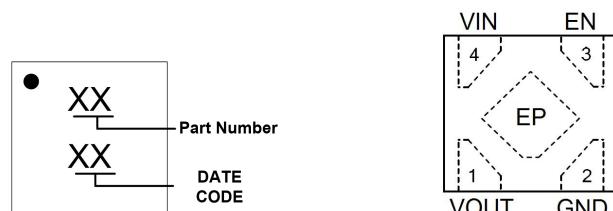
● Ordering Information

AS9107XXXX					
Output Voltage:		Package:	Packing:	Temperature Grade:	
12:1.2V	15:1.5V	D:DFN1010-4L	R:Tape&Reel	N: -40°C~85°C	
18:1.8V	28: 2.8V	ST: SOT23-5L		Y: -40°C~125°C	
30:3.0V	33: 3.3V	S3: SOT23-3L		Z: -45°C~150°C	
		Y: SOT89-3L			

Part Number	Driver Capability	Package Type	Package Qty	Temperature	Eco Plan
AS9107-XXDRN	300mA	DFN1010-4L	7-in reel 10000pcs/reel	-40~85°C	RoHS
AS9107-XXS3RN	300mA	SOT23-3L	7-in reel 3000pcs/reel	-40~85°C	RoHS
AS9107-XXSTRN	300mA	SOT23-5L	7-in reel 3000pcs/reel	-40~85°C	RoHS
AS9107-XXYRN	300mA	SOT89-3L	7-in reel 1000pcs/reel	-40~85°C	RoHS
AS9107-XXDRY	300mA	DFN1010-4L	7-in reel 10000pcs/reel	-40~125°C	RoHS
AS9107-XXS3RY	300mA	SOT23-3L	7-in reel 3000pcs/reel	-40~125°C	RoHS
AS9107-XXSTRY	300mA	SOT23-5L	7-in reel 3000pcs/reel	-40~125°C	RoHS
AS9107-XXYRY	300mA	SOT89-3L	7-in reel 1000pcs/reel	-40~125°C	RoHS
AS9107-XXDRZ	300mA	DFN1010-4L	7-in reel 10000pcs/reel	-45~150°C	RoHS
AS9107-XXS3RZ	300mA	SOT23-3L	7-in reel 3000pcs/reel	-45~150°C	RoHS
AS9107-XXSTRZ	300mA	SOT23-5L	7-in reel 3000pcs/reel	-45~150°C	RoHS
AS9107-XXYRZ	300mA	SOT89-3L	7-in reel 1000pcs/reel	-45~150°C	RoHS

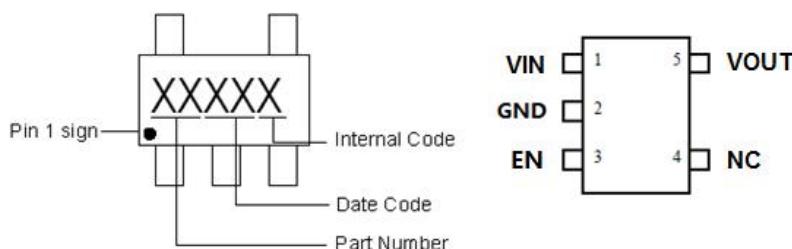
● Marking Information & Pin Assignment

DFN1010-4L

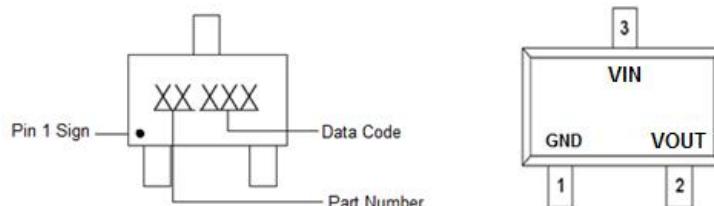


Pin Name	Pin No. DFN1010-4L	I/O	Pin Function	
VOUT	1	O	Output Pin	
GND	2	P	Ground	
EN	3	I	Pull this pin high to enable IC, pull this pin low to shutdown IC. Floating this pin will shutdown due to the built-in pull low resistor.	
VIN	4	P	Input Power Supply	
EP	-	-	Please Connected to GND.	

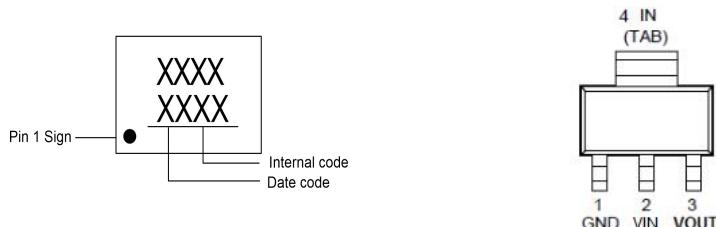
SOT23-5L (Fixed Output Voltage):



Pin Name	Pin No. SOT23-5L	I/O	Pin Function	
VIN	1	P	Input Power Supply	
GND	2	P	Ground	
EN	3	I	Pull this pin high to enable IC, pull this pin low to shutdown IC.	
NC	4	-	Not Connected.	
VOUT	5	O	Output Pin	

SOT23-3L

Pin Name	Pin No. SOT23-3L	I/O	Pin Function	
VIN	3	P	Input Power Supply	
GND	1	P	Ground	
VOUT	2	O	Output Pin	

SOT89-3L:

Pin Name	Pin No. SOT89-3L	I/O	Pin Function	
VOUT	3	O	Output Pin	
GND	1	P	Ground	
VIN	2	P	Input Power Supply	
TAB	4	P	Please Connected to VIN.	

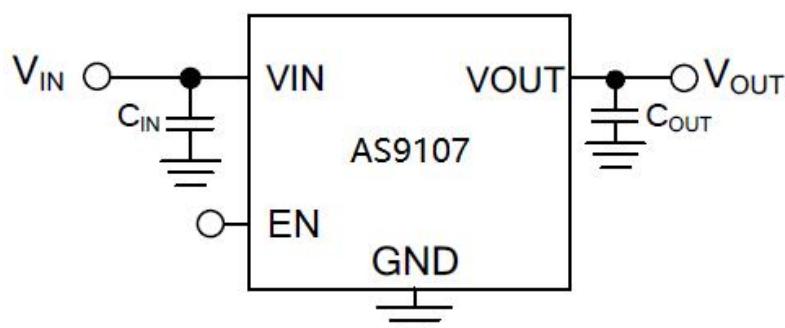
● Typical Application Circuit

Figure 1, Typical Application Circuit of AS9107

Note: To prevent oscillation, it is recommended to use 1μF X7R or X5R dielectric capacitors if ceramics are used as input/output capacitors (please refer to application information).

- Block Diagram

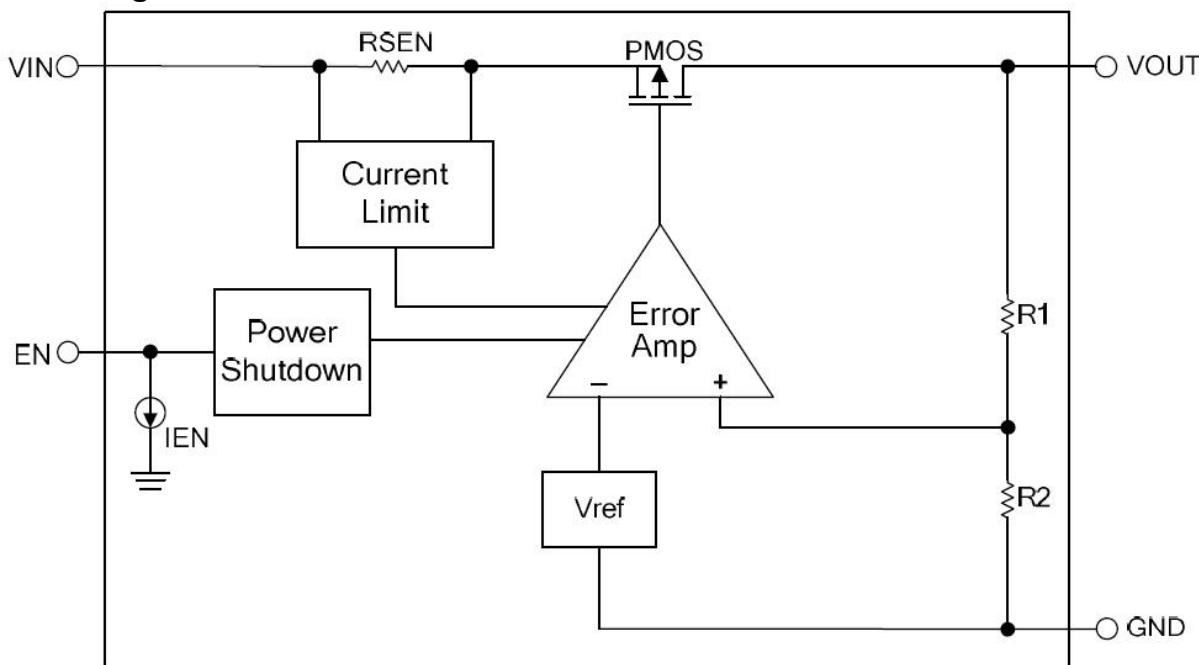


Figure 2, Block Diagram of AS9107

- Absolute Maximum Ratings¹ ($T_A=25^\circ\text{C}$, unless otherwise noted)

Parameter	Symbol	Rating	Unit
V_{IN} Pin to GND	V_{IN}	-0.3 to +6.5	V
All Other Pins to GND	-	-0.3 to $V_{IN} + 0.3$	V
Storage Temperature Range	T_S	-55 to +150	°C
Operating Junction Temperature Range	T_J	-45 to +150	°C
Maximum Soldering Temperature (at leads, 10 sec)	T_{LEAD}	260	°C

- Recommended Operating Conditions²

Parameter	Symbol	Rating	Unit
V_{IN} Pin Voltage to GND	V_{IN}	+2.0 to +5.5	V
Output Current	I_{OUT}	Up to 300	mA
Operating Temperature Range	T_{OP}	-45 to +150	°C
Maximum Thermal Resistance	DFN1010-4L	Θ_{JA}	130 °C/W
	SOT23-3L/5L	Θ_{JA}	200 °C/W
	SOT89-3L	Θ_{JA}	200 °C/W
Maximum Power Dissipation	$T_A < 25^\circ\text{C}$	P_D	0.50 W

Note: 1: Stresses above those listed in absolute maximum ratings may cause permanent damage to the device. Functional operation at conditions other than the operating conditions specified is not implied. Only one absolute maximum rating should be applied at any one time.

2: The device is not guaranteed to function outside of its operating conditions.

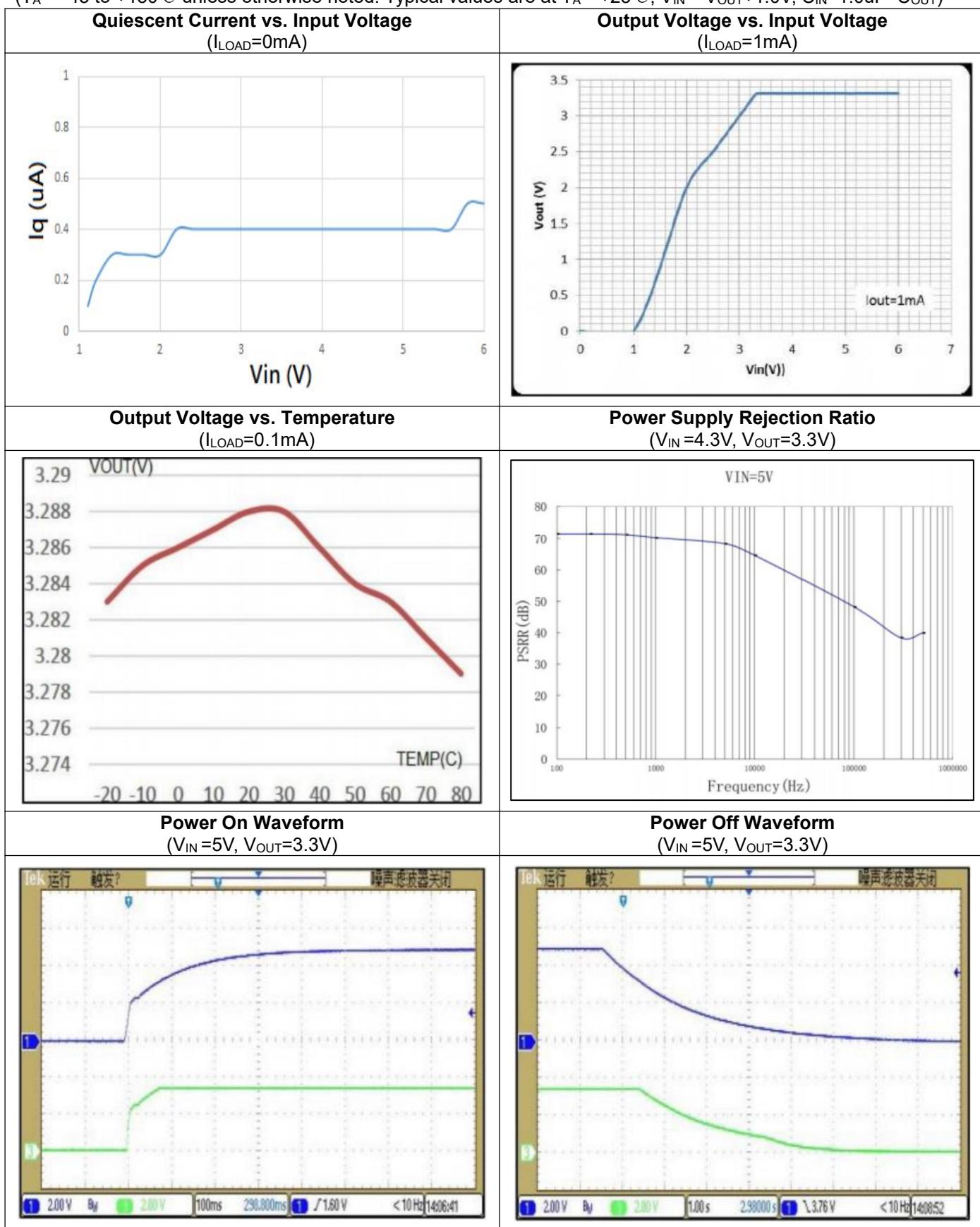
● Electrical Characteristics

($T_A = -45$ to $+150^\circ\text{C}$ unless otherwise noted. Typical values are at $T_A = +25^\circ\text{C}$, $V_{IN} = V_{OUT} + 1.0\text{V}$, $C_{IN} = 1.0\text{uF} = C_{OUT}$)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
Supply						
V_{IN}	Input Voltage		2.0	-	5.5	V
I_Q	Supply Current		-	5.0	-	μA
I_{SHDN}	Supply Current Shutdown	$V_{EN} = 0\text{V}$	-	0.01	-	μA
Output Current						
ΔV_{OUT}	Output Voltage Accuracy	$I_O = 1\text{mA}$, $V_{OUT} > 1.8\text{V}$	-2.0	-	2.0	%
		$I_O = 1\text{mA}$, $V_{OUT} \leq 1.8\text{V}$	-2.0	-	2.0	%
I_{OUT}	Output Current		-	300	-	mA
I_{LIMIT}	Output Current Limit		350	550	-	mA
V_{DROP}	Dropout Voltage	$I_O = 100\text{mA}$, $V_{OUT} = 3.3\text{V}$	-	100	-	mV
		$I_O = 300\text{mA}$, $V_{OUT} = 3.3\text{V}$	-	300	-	mV
$V_{Temp.}$	Temperature Coefficient	$I_O = 40\text{mA}$, $V_{IN} = 5.0\text{V}$,	-	100	-	$\text{ppm}/^\circ\text{C}$
ΔV_{LOAD}	Load Regulation	$I_O = 1\text{mA}$ to 300mA	-	0.1	1.0	%
ΔV_{LINE}	Line Regulation	$I_O = 1\text{mA}$, $V_{IN} = V_{OUT} + 1\text{V}$ to 5V	-	0.1	0.15	$\%/\text{V}$
PSRR	Ripple Rejection $I_{OUT} = 100\text{mA}$	$f_{RIPPLE} = 1\text{KHz}$, $V_{OUT} = 1.8\text{V}$	-	75	-	dB
		$f_{RIPPLE} = 10\text{KHz}$, $V_{OUT} = 1.8\text{V}$	-	65	-	dB
V_{NOISE}	Output Noise Voltage	$I_{OUT} = 30\text{mA}$, $BW = 10\text{HZ} \sim 100\text{KHZ}$	-	65	-	μV_{RMS}
I_{CFB}	Current Foldback	$R_{LOAD} = 1.0\Omega$	-	90	-	mA
Enable (/SHDN)						
V_{ENL}	EN OFF Threshold	$V_{IN} = 5\text{V}$, V_{EN} Falling	0	-	0.4	V
V_{ENH}	EN ON Threshold	$V_{IN} = 5\text{V}$, V_{EN} Rising	1.4	-	V_{IN}	V
I_{EN}	EN Pin Bias Current		-	0.1	1	μA
Thermal Shutdown						
T_{SD}	Thermal Shutdown		-	160	-	$^\circ\text{C}$
T_{SDHY}	Thermal Shutdown Hysteresis		-	20	-	$^\circ\text{C}$

● Typical Characteristics—AS9107

($T_A = -45$ to $+150^\circ\text{C}$ unless otherwise noted. Typical values are at $T_A = +25^\circ\text{C}$, $V_{IN} = V_{OUT} + 1.0\text{V}$, $C_{IN} = 1.0\mu\text{F} = C_{OUT}$)



● Application Information

Typical Application Circuits

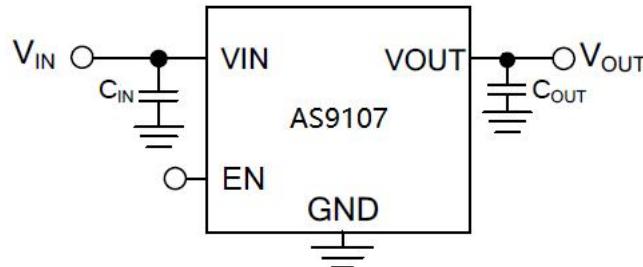


Figure 3, Typical Application Circuit of AS9107

The AS9107 series are low dropout linear regulators that could provide 300mA output current at low input voltage. Besides, current limit and on chip thermal shutdown features provide protection against any combination of over-load or ambient temperature which could cause junction temperature exceeding maximum rating.

Output and Input Capacitor

The AS9107 regulator is designed to be stable with a wide range of output capacitors. The ESR of the output capacitor affects stability. Larger value of the output capacitor decreases the peak deviations and improves transition response for larger current changes.

The capacitor types (aluminum, ceramic and tantalum) have different characterizations such as temperature and voltage coefficients. All ceramic capacitors are manufactured with a variety of dielectrics, each with different behavior across temperature and applications. Common dielectrics used are X5R, X7R and Y5V. It is recommended to use 1μF to 10μF X5R or X7R dielectric ceramic capacitors with 30mΩ to 50mΩ E R range between device outputs to ground for transient stability. The AS9107 is designed to be stable with low ESR ceramic capacitors and higher values of capacitors, and ESR could improve output stability. So the ESR of output capacitor is very important because it generates a zero to provide phase lead for loop stability.

There are no requirements for the ESR on the input capacitor, but its voltage and temperature coefficient have to be considered for device application environment.

Protection Feature

In order to prevent overloading or thermal condition from damaging the device, AS9107 regulator has internal thermal and current limit functions designed to protect the device. It will rapidly shut off PMOS pass element during over-loading or over-temperature condition.

Thermal Consideration

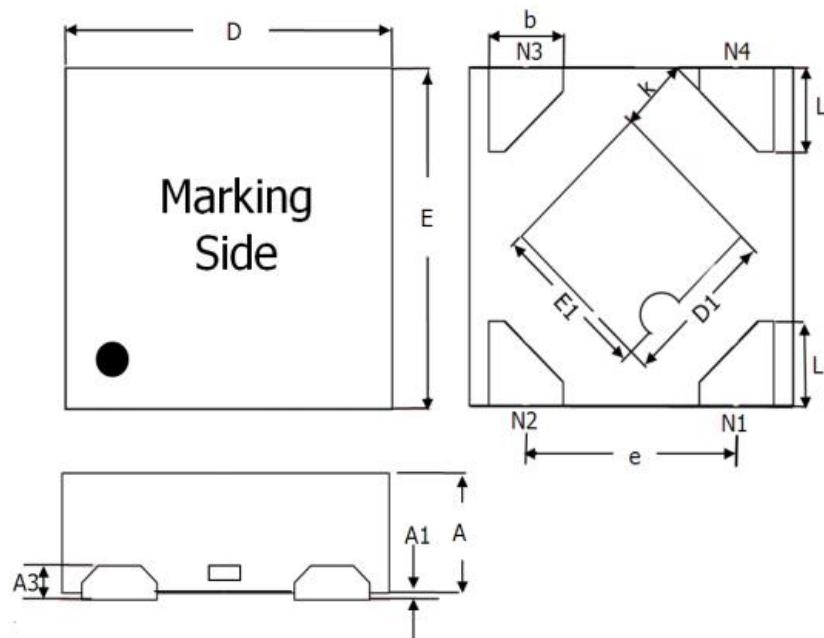
The power handling capability of the device will be limited by allowable operation junction temperature (150°C). The power dissipated by the device will be estimated by $P_D = I_{OUT} (V_{IN} - V_{OUT})$. The power dissipation should be lower than the maximum power dissipation listed in "Absolute Maximum Ratings" section.

Active / Shutdown Input Operation

The AS9107 is turned off by pulling the EN pin low and turned on by pulling it high. If this feature is not used, the EN pin should be connected to V_{IN} to keep the regulator output available all the time.

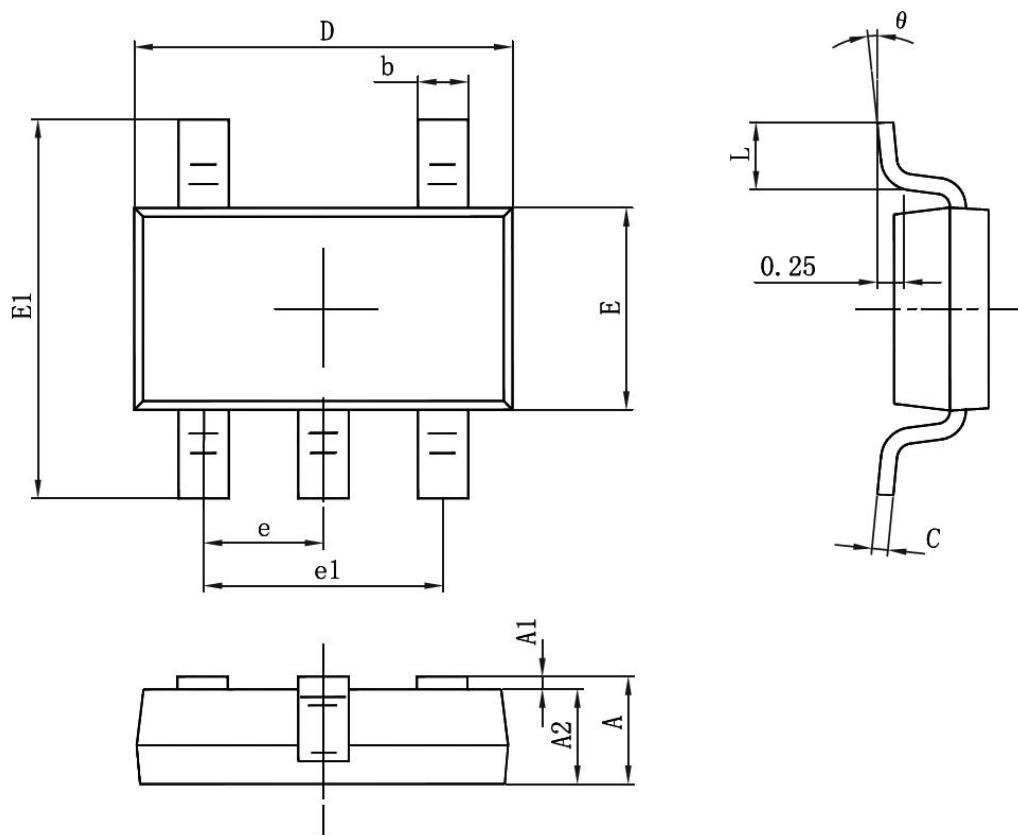
● Package Information

DFN1010-4L:



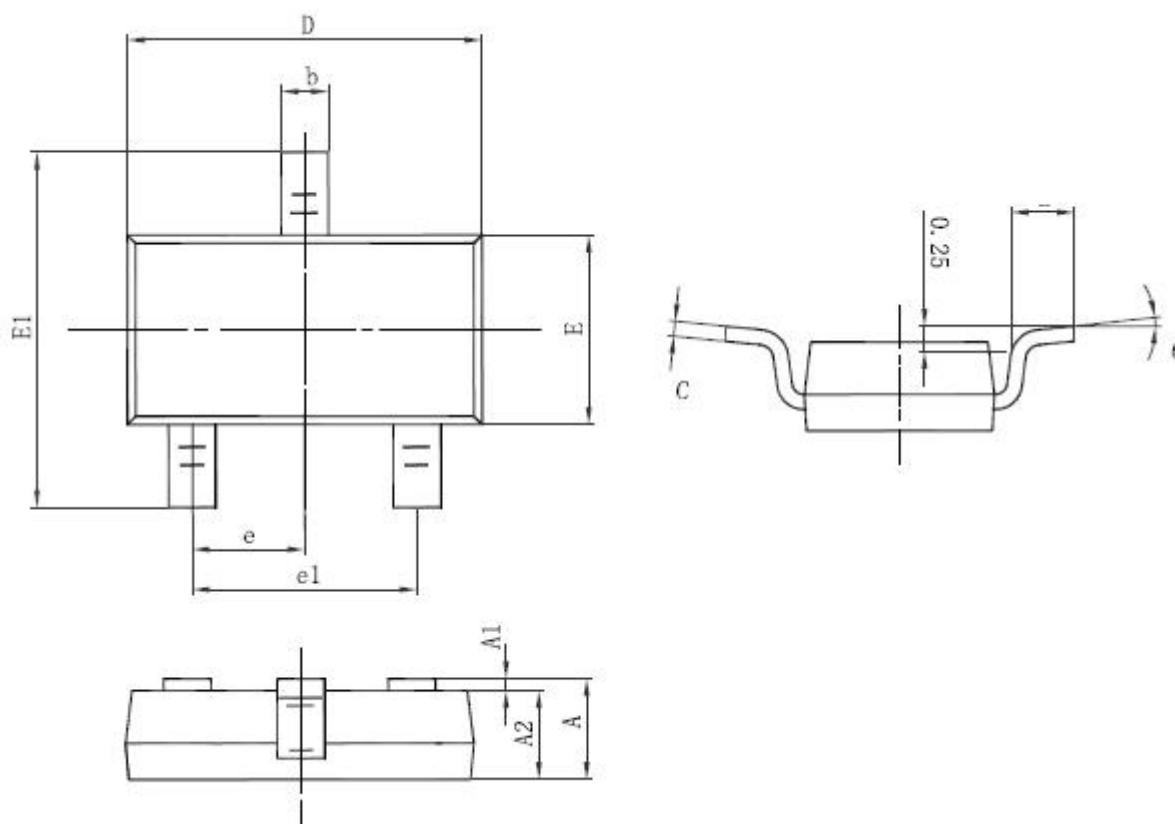
Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.340	0.400	0.014	0.016
A1	0.000	0.050	0.000	0.002
A3	0.152 BSC		0.006 BSC	
D	0.950	1.050	0.038	0.042
E	0.950	1.050	0.038	0.042
D1	0.450	0.550	0.018	0.022
E1	0.450	0.550	0.018	0.022
k	0.211 BSC		0.008 BSC	
b	0.180	0.280	0.0072	0.0112
e	0.625 BSC		0.025 BSC	
L	0.200	0.300	0.008	0.012

SOT23-5L:



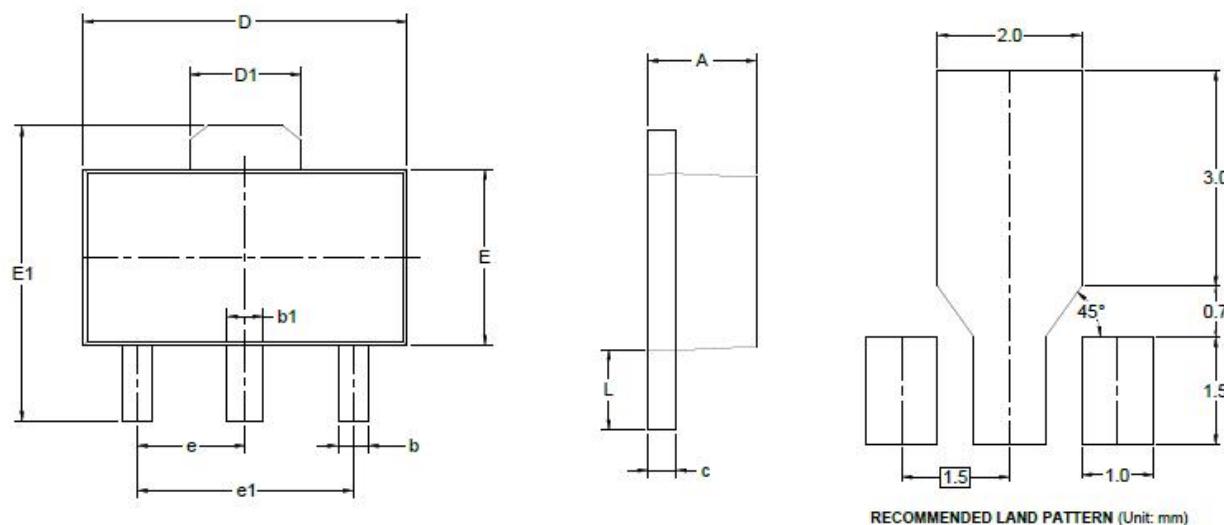
Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.900	1.450	0.036	0.058
A1	0.000	0.150	0.000	0.006
A2	0.900	1.300	0.036	0.052
b	0.300	0.500	0.012	0.020
C	0.080	0.200	0.003	0.008
D	2.800	3.000	0.112	0.120
E	1.500	1.700	0.060	0.068
E1	2.600	3.000	0.104	0.120
e	0.95(BSC)		0.037(BSC)	
e1	1.90(BSC)		0.075(BSC)	
L	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°

SOT23-3L:

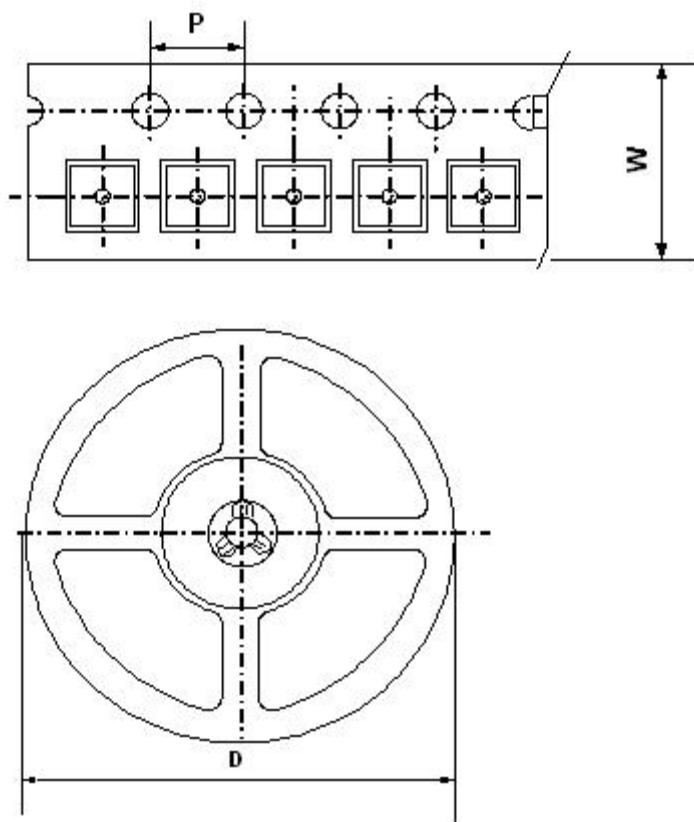


Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
c	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E	1.500	1.700	0.059	0.067
E1	2.650	2.950	0.104	0.116
e	0.950(BSC)		0.037(BSC)	
e1	1.900(BSC)		0.075(BSC)	
L	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°

SOT89-3L:



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	1.400	1.600	0.055	0.063
b	0.320	0.520	0.013	0.020
b1	0.400	0.580	0.016	0.023
c	0.350	0.440	0.014	0.017
D	4.400	4.600	0.173	0.181
D1	1.550 REF		0.061 REF	
E	2.300	2.600	0.091	0.102
E1	3.940	4.250	0.155	0.167
e	1.500 TYP		0.060 TYP	
e1	3.000 TYP		0.118 TYP	
L	0.900	1.200	0.035	0.047

■ Packing Information

Package Type	Carrier Width(W)	Pitch(P)	Reel Size(D)	Packing Minimum
DFN1010-4L	8.0±0.1 mm	2.0±0.1 mm	180±1 mm	10000pcs
SOT23-3L	8.0±0.1 mm	4.0±0.1 mm	180±1 mm	3000pcs
SOT23-5L	8.0±0.1 mm	4.0±0.1 mm	180±1 mm	3000pcs
SOT89-3L	8.0±0.1 mm	4.0±0.1 mm	180±1 mm	1000pcs

Note: Carrier Tape Dimension, Reel Size and Packing Minimum